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Attorney Docket No. .130349
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Date: February 20, 2002
STOCKSTAD	:	Group Art Unit: Unknown
Serial No.: Unknown	:	Examiner: Unknown
Filed: Herewith	:	
Title: INTEGRATED CIRCUIT AND METHOD OF CONTROLLING OUTPUT IMPEDANCE	:	



INFORMATION DISCLOSURE STATEMENT

Box Patent Application
Commissioner of Patents
Washington, DC 20231

Sir:

Attached hereto is a completed Form PTO-1449 listing patents, publications, applications, or other information that the applicant believes may be material to the examination of the subject patent application, with copies of each such item enclosed herewith. It is requested that the cited patents, publications, applications, or other information be made of record in the examination of this application.

This information disclosure statement is being filed (1) within three months of the filing date of a national application other than a continued prosecution application, (2) within three months of the date of entry of the national stage, (3) before the mailing of a first Office Action on the merits, or (4) before the mailing of a first Office Action after the filing of a request for continued examination. Accordingly, no fee is required. The commissioner is, however, authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 02-4467.

Title: INTEGRATED CIRCUIT AND METHOD OF CONTROLLING
OUTPUT IMPEDANCE

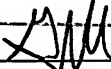
Serial No.: Unknown

Information Disclosure Statement

No representation is made that a search has been performed. No admission is made that the information cited in this statement is, or is considered to be, material to the patentability of the subject patent application.

Respectfully submitted,

Dated: 20 February 2002

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CERTIFICATE OF EXPRESS MAILING UNDER 37 C.F.R. 1.10.

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